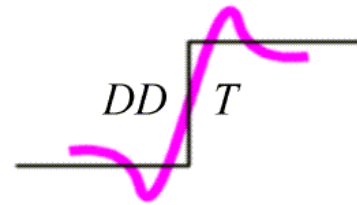


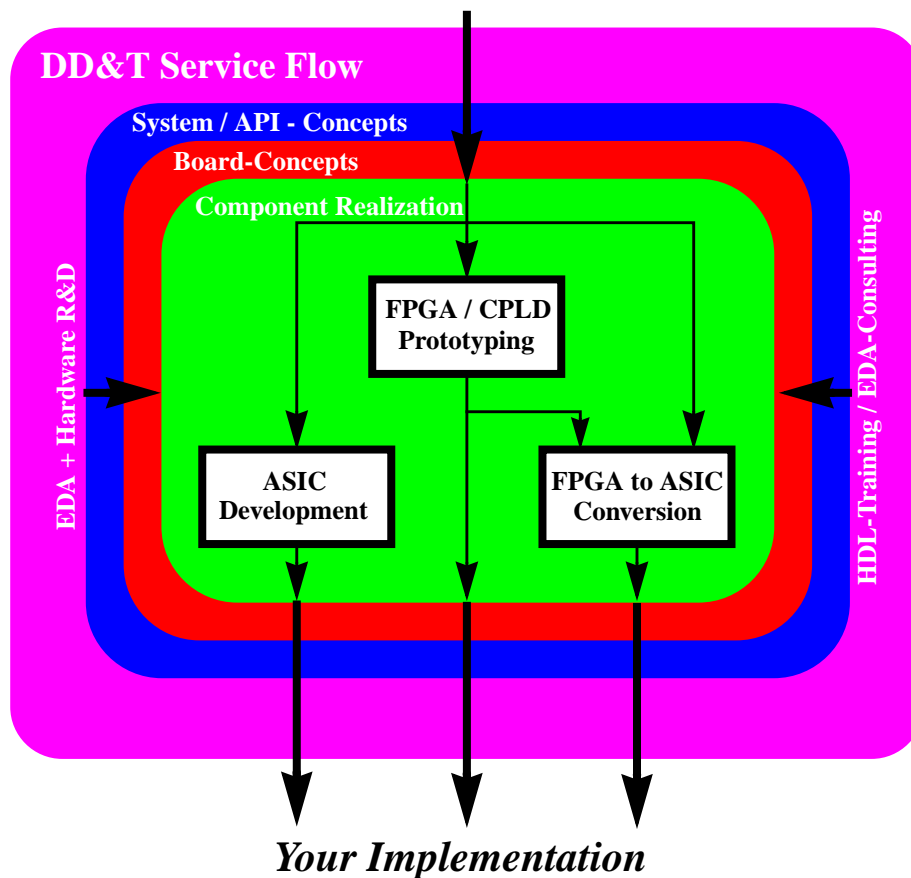
# DD&T -Digital Design & Technology

Gesellschaft für Hard- und Software Entwicklung mbH

Saving your time while reducing your risk!



## Your Requirements



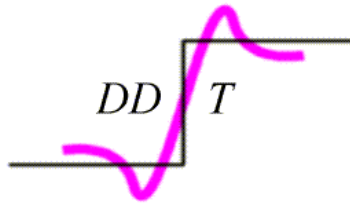
E-Mail: [ddt@dd-t.com](mailto:ddt@dd-t.com) • URL: <http://www.dd-t.com>

**DD&T**  
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Gesellschaft für Hard- und  
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Fax: (+49) 07121 43308-19

**Sitz der Gesellschaft:**  
Tübingen • HRB1762  
**Geschäftsführer:**  
Dr. rer. nat. Oliver Renz  
Dipl.-Inf. Urs Kanus

**So finden Sie uns:**  
Lageplan im Internet unter  
[www.dd-t.com/location.htm](http://www.dd-t.com/location.htm)



## *About DD&T*

Through years of experience, Digital Design & Technology (DD&T) is able to offer the following services:

- [Hardware-Development from concept to ASIC or board.](#)
- [EDA-Consulting and EDA- and HDL-courses.](#)
- [Transfer of technology between industry and research institutions.](#)
- [Graphics-Hardware research and development.](#)

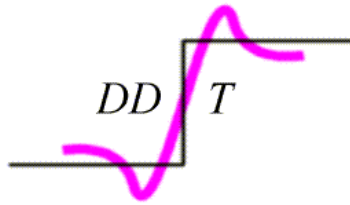
DD&T was founded by Dr. Oliver Renz with support of the Ministry of Science and Research, Baden-Württemberg. Joining forces with Dipl. Inform. Urs Kanus broadened the variety of services DD&T can offer.

DD&T's mission is to provide small and medium sized companies (SMEs) with a small but efficient ASIC- and FPGA development team, which is beneficial for SMEs in many ways:

- [Short development time](#)
- [Developments will be carried out with state-of-the-art tools, methods and technology.](#)
- [We choose the most suited technology for your project.](#)

DD&T is coordinating a network of free-lance hardware developers. All developers have a profound knowledge with state-of-the-art EDA-tools and methods.

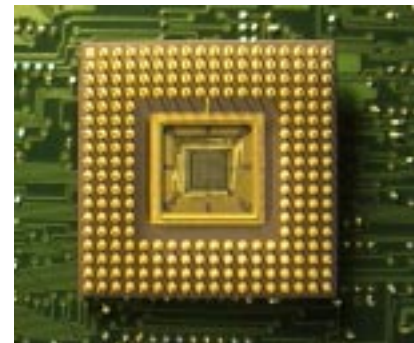
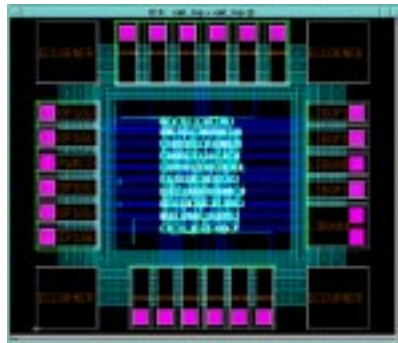
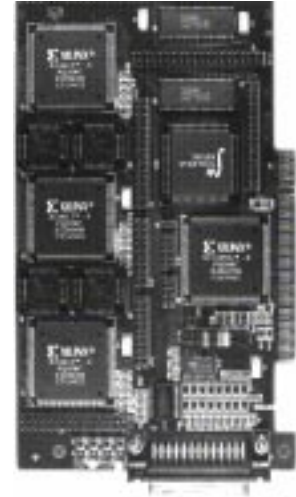
The following pages give an overview of our variety of services.



## *Hardware Development from Concept to Implementation*

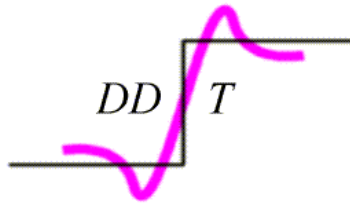
In accordance with the customer's requirements we perform all or part of the following design steps:

- Feasibility analysis (i.e., Performance, Area, Cost)
- Specification (Board-I/O, Chip-I/O, Setup)
- Architecture proposal (i.e., as VHDL or Verilog Model)
- Choice of technology
- Design and verification of the gate-level circuit
- Conversion or extension of existing circuits
- Foundry interaction (Testvectors and all other prerequisites for chip manufacturing)
- Board integration (i.e., Board-Debugging)
- Software/Hardware interface (Drivers and APIs)



## *EDA-Consulting and Training*

Years of experience with every aspect of EDA-tools and hardware design enable us to guide your choice of EDA-tools and methodology. We provide training, in case your team needs to be updated to the latest advances in HDL-based design methods. Training courses can be individually tailored to the needs of your design team.



## *Technology Transfer between Research and Industry*

An additional goal of DD&T is to nurture the technology transfer between research institutions and industry through the following activities:

- Graduates with specialized knowledge in the field of hardware development get a chance to apply their know-how in real world projects.
- University development results can be introduced to the market by DD&T, or developed further by DD&T.
- Universities can be guided towards the needs of industry, e.g, with practice oriented training.

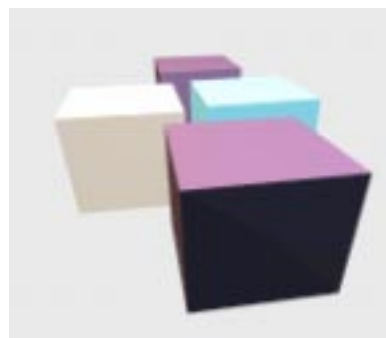
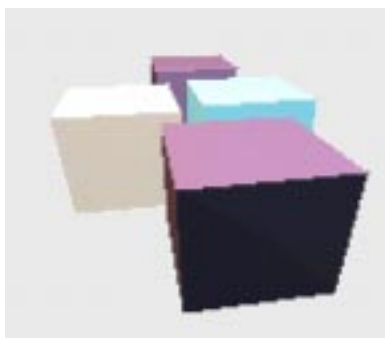
## *Graphics Hardware Research and Development*

Our experience in the development of graphics hardware together with our close collaboration with leading research facilities in this area allows us research and development for:

- PCI/AGP based 2D/3D graphics accelerators
- Systems for volume visualisation
- High quality and high performance for multimedia applications through hardware implementation of new algorithms (i.e., antialiasing).
- Increased Performance by using state-of-the-art chip-sets, bus- and memory-architectures.



Hardware-acceleration  
for  
Volume Visualisation



Real-time Display and Higher Quality for 3D Computer Graphics